

Fig.1

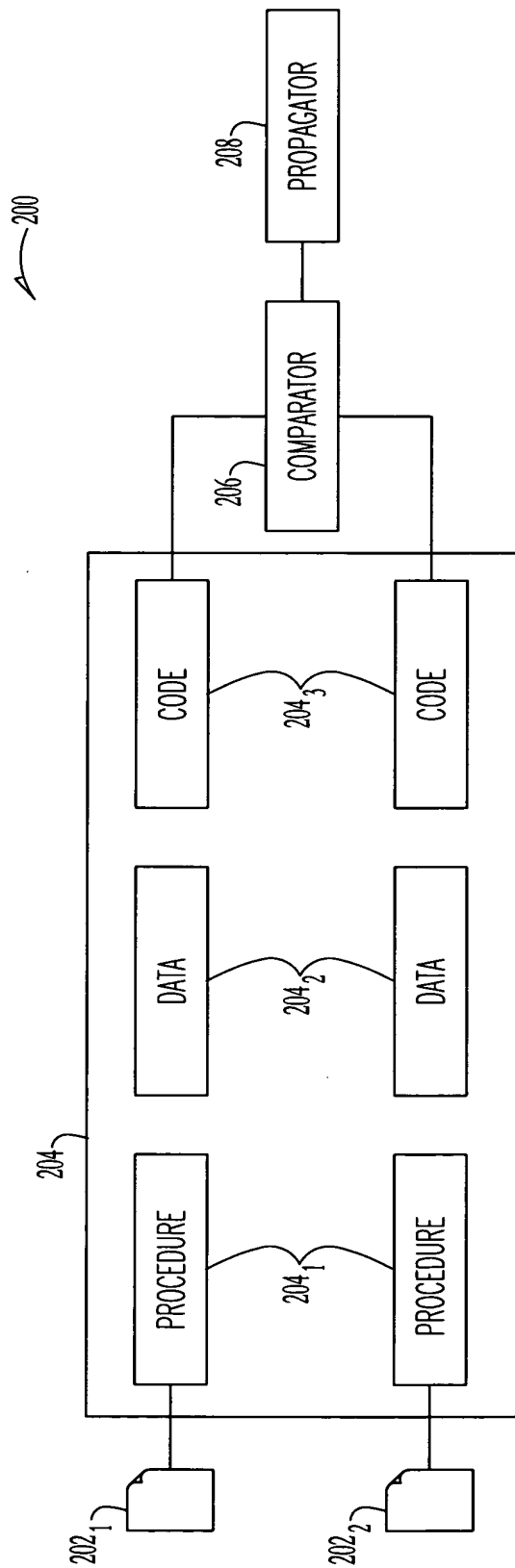


Fig.2

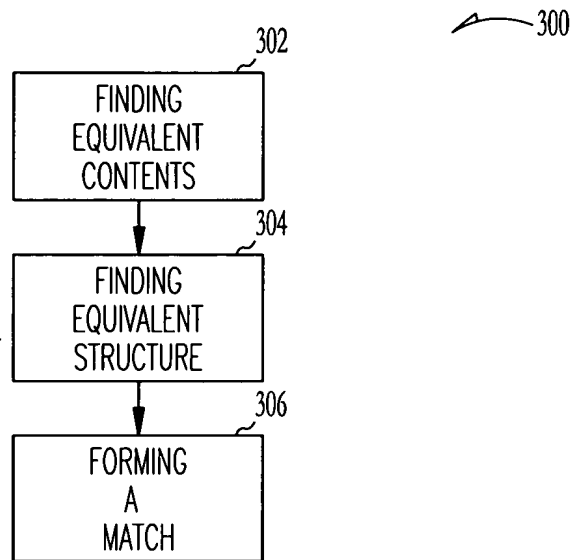


Fig.3

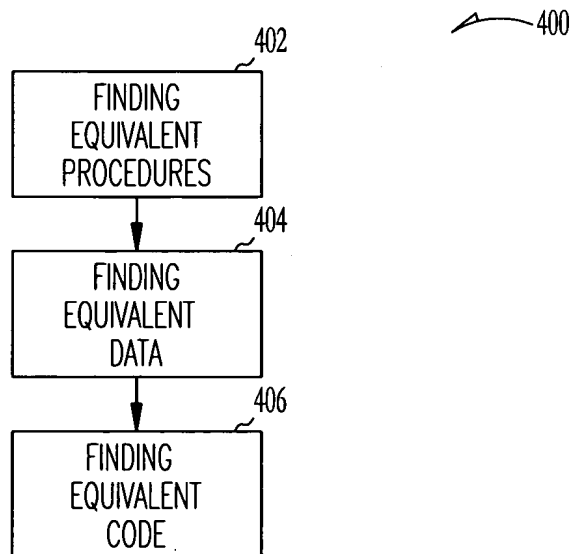


Fig.4

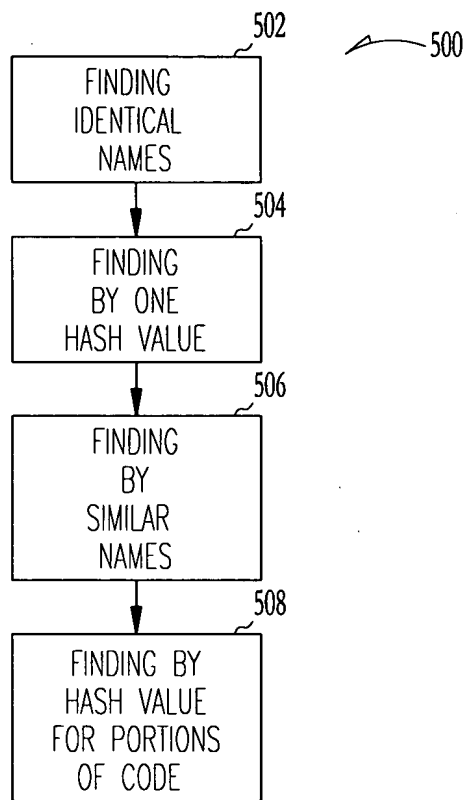


Fig.5

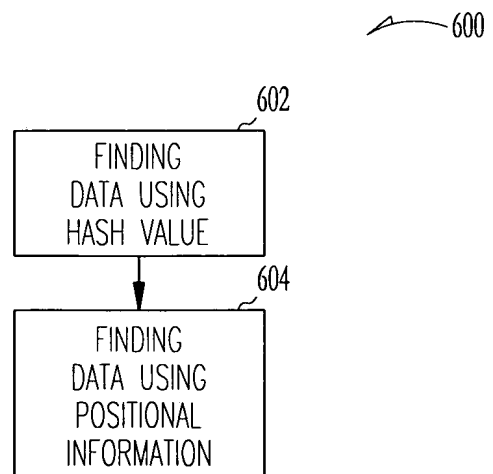


Fig.6

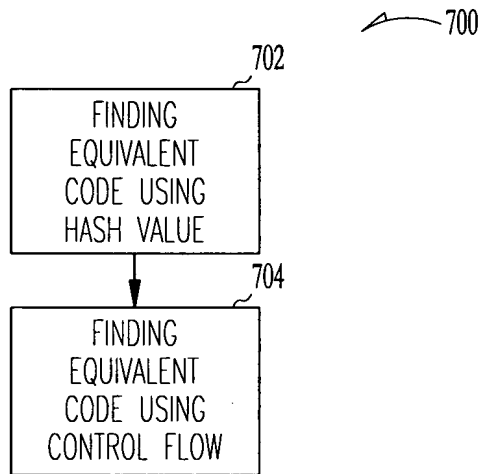


Fig.7

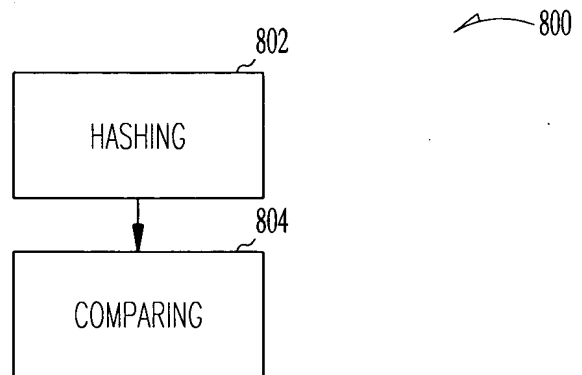


Fig.8

FUZZINESS LEVEL	NUMERICAL ADDRESS OFFSET	REGISTER ALLOCATION	BLOCK ADDRESS OPERAND	OPERAND	OPCODE
0	ALL	ALL	<ul style="list-style-type: none"> TARGET BLOCK'S MATCH TARGET BLOCK'S EXTENDED NAME TARGET PROCEDURE NAME OR BRANCH OFFSET WITHIN PROCEDURE TARGET BLOCK'S DISTANCE FROM BEGINNING OF PROCEDURE 	ALL	ALL
1	NONE	EAX/ECX/EDX: DEPENDENCY ONLY	SAME AS LEVEL 0	ALL	ALL
1A	SAME AS LEVEL 1, BUT INCLUDES ONLY THE LAST INSTRUCTION IN EACH BLOCK				
2	NONE	EAX/ECX/EDX: DEPENDENCY ONLY	<ul style="list-style-type: none"> TARGET BLOCK'S MATCH TARGET BLOCK'S EXTENDED NAME TARGET PROCEDURE NAME OR BRANCH OFFSET WITHIN PROCEDURE 	ALL	ALL
3	NONE	EAX=ECX=EDX EBX=EDI=ESI	TARGET PROCEDURE NAME OR BRANCH DIRECTION WITH PROCEDURE	NO IMMEDIATE NONE FOR RETURN	ALL
3A	SAME AS LEVEL 3, BUT INCLUDES ONLY THE LAST INSTRUCTION IN EACH BLOCK				
4	NONE	N/A	N/A	TYPE ONLY	ALL
5	NONE	N/A	N/A	NONE	GROUP

Fig.9